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APPLICANTS									
Steve C. Huang, Saratoga, CA; Yong Fan, Fremont, CA; Ihao Chen, San Jose, CA; *** CONTINUING DATA **********************************									
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ADDRESS 30349 JACKSON & CO., LLP 6114 LA SALLE AVENUE SUITE 507 OAKLAND , CA 94611-2802									
TITLE Method and system for providing fast design for testability prototyping in integrated circuit designs									
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